

Table of Contents

Chapter 1: Nine Topologies

Introduction	3
1.1 The Nine Most Useful Power Topologies	4
1.1.1 Buck Converter.....	4
1.1.2 Forward Converter (<i>Isolated Buck Converter</i>)	6
1.1.3 Two-Switch Forward Converter (<i>Isolated Buck Converter</i>) ..	7
1.1.4 Half-Bridge Converter (<i>Isolated Buck Converter</i>).....	8
1.1.5 Full-Bridge Converter (<i>Isolated Buck Converter</i>)	9
1.1.6 Push-Pull Converter (<i>Isolated Buck Converter</i>)	10
1.1.7 Boost Converter	11
1.1.8 Buck-Boost Converter.....	12
1.1.9 Flyback Converter (<i>Isolated Buck-Boost Converter</i>)	13
1.2 Cascaded Converters	14
1.3 Other Converters	14
1.4 Summary	14
References	14

Chapter 2: Modeling Power Topologies

Introduction	15
2.1 Power Supply Modeling Approaches	16
2.1.1 Modeling Power Supplies.....	16
2.1.2 Equation References.....	16
2.1.3 Modeling Techniques for PWM Converters.....	17
2.2 Buck Converter with Voltage-Mode Control.....	18
2.2.1 Equations for the Buck Converter	18
2.2.2 Engineering Design Tools.....	20
2.2.3 Buck Converter Transfer Functions	20
2.2.4 Important Buck Characteristics	22
2.2.5 Buck Summary	23
2.3 Boost Converter with Voltage-Mode Control.....	23
2.3.1 Equations for the Boost Converter	23
2.3.2 Boost Converter Right-Half-Plane Zero	24
2.3.3 Boost Converter Transfer Function Plots.....	24
2.3.4 Important Boost Characteristics	27
2.3.5 Boost Converter Voltage-Mode Software	27
2.3.6 Boost Summary	27
2.4 Buck-Boost Converter with Voltage-Mode Control	28
2.4.1 Equations for the Buck-Boost Converter	28
2.4.2 Buck-Boost Converter Right-Half-Plane Zero	29
2.4.3 Important Buck-Boost Characteristics	31
2.4.4 Buck-Boost Converter Voltage-Mode Software	31
2.4.5 Buck-Boost Summary	32
References.....	32

Chapter 3: Voltage-Mode Compensation

Introduction	33
3.1 Power Supply with Feedback Control	35
3.2 Type I Compensation Amplifier	37
3.3 Type II Compensation Amplifier	37
3.4 Type III Compensation Amplifier	40
Voltage-Mode Buck Compensation Rules	42
3.5 Compensation for Voltage-Mode Buck Converter	42
3.6 Compensation for Voltage-Mode Boost and Flyback Converters	47
Voltage-Mode Boost and Buck-Boost Compensation Rules	48
3.7 Voltage-Mode Control Summary	50

Chapter 4: Current-Mode Control Modeling

Introduction	53
4.1 Current-Mode Control	54
4.2 Basic Current-Source Dynamics	54
4.3 Subharmonic Oscillation	55
4.4 Transfer Function for Subharmonic Oscillation	57
4.5 Buck Converter with Current-Mode Control	58
4.6 Boost Converter with Current-Mode Control	60
4.7 Buck-Boost Converter with Current-Mode Control	63
4.8 Current Mode Summary	65
References	65

Chapter 5: Current-Mode Compensation

Introduction	67
5.1 Current Mode Feedback Loop	68
5.2 Buck Current-Mode Feedback Compensation	70
Current-Mode Buck Compensation Rules	71
Current-Mode Boost and Buck-Boost Compensation Rules	73
5.3 Boost and Buck-Boost Current-Mode Feedback Compensation	73
5.4 Summary	76
References	76

Chapter 6: Frequency Response Measurements

Introduction	79
6.1 Switching Power Supplies – Ultra Wide Band Circuits ...	80
6.1.1 Control Frequencies	81
6.1.2 Noise Frequencies	82
6.1.3 Control Signal and Noise All At Once	82
6.2 Extracting Test Signals from Noise	84
6.2.1 Single Frequency Measurements	84
6.2.2 Frequency Response Analyzer	85
6.3 Injecting Signals into the Power Supply	87
6.3.1 Audiosusceptibility Measurement	87
6.3.2 Input Impedance Measurement	88
6.3.3 Output Impedance Measurement	89

6.3.4 Control-to-Output Measurement	90
6.3.5 Loop Gain Measurement	91
6.4 Loop Gain Measurement Injection Technique	92
6.4.1 Power Supply Loop Gain.....	92
6.4.2 Breaking the Loop with the Injection Signal	93
6.4.3 Loop Injection at Different Frequencies	94
6.5 Loop Gain Signal Injection Size.....	97
6.5.1 Fixed Loop Gain Injection Signal	97
6.5.2 Variable Loop Gain Injection Signal	100
6.5.3 Loop Gain Measurements without Predictions....	101
6.6 Loop Gain Stability Assessment	102
6.6.1 Phase Margin of a Control Loop	102
6.6.2 Gain Margin of a Control Loop	103
6.6.3 Conditionally Stable Systems	104
6.6.4 Loop Gains with Multiple Crossover Frequencies..	105
6.7 Step-Load Transient Testing.....	106
6.7.1 Power Supply Transient Response.....	106
6.7.2 Boost Converter Transient Response - Too Much Gain	108
6.7.3 Buck Converter Transient Response - Too Little Gain.....	110
6.7.4 Flyback Transient Response - Incorrect Compensation	112
References.....	114

Chapter 7: System Issues

Introduction	115
7.1 Designing with the TL431	116
7.1.1 Operational Amplifier Feedback	116
7.1.1 Using the TL431 as a Type II Amplifier	117
7.1.2 Solution with Isolation	118
7.1.3 Compensation – Low Frequency	119
7.1.4 Compensation – Mid Frequency	120
7.1.5 Compensation – High Frequency	121
7.1.6 Complete Compensation	122
7.1.7 TL431 Loop Measurement	123
7.1.8 TL431 with Second Stage Filter	124
7.1.9 TL431 Summary	126
7.2 Adding Capacitors to a Power Supply	126
7.2.1 Forward Converter Design Step Load	126
7.2.2 Step Load with Increased Load Capacitance	128
7.2.3 Transfer Functions with Increased Load Capacitance	129
7.2.4 Added Capacitance and Recompensated Loop ...	130
7.2.5 Module Design with Flexibility	131

Chapter 8: Input Filter Interactions

Introduction	135
8.1 Simple Filter Explanation	136
8.2 The Generalized Impedance Criteria	138
8.3 Which input impedance should you use?	139
8.4 Design Case 1: $Z_{out} \ll \text{Open Loop } Z_{in}$	140

8.5 Design Case 2: $Z_{out} \ll \text{Closed Loop } Z_{in}$	142
8.6 Design Case 3: $Z_{out} > \text{Closed Loop } Z_{in}$	144
8.7 Design Case 4: The “Magic” of Feedforward	146
8.8 Design Case 5: Current-Mode Control	148
8.9 Defining the “Filter” and Power Supply	150
8.10 Making Measurements	152
8.11 Input Filter Summary	154
References	154

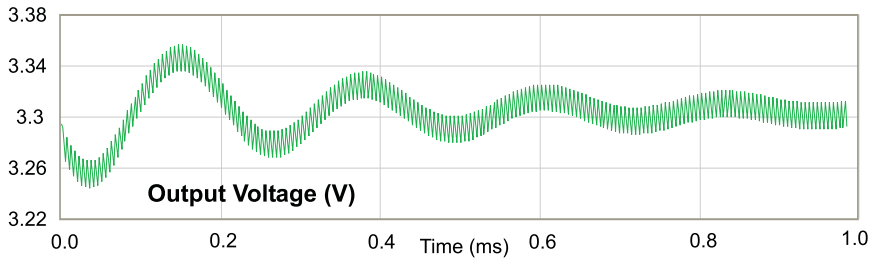
Chapter 9: Noise Issues

Introduction	155
9.1 Power Supply Instability Issues	156
9.1.1 Typical Control Circuit	157
9.1.2 Amplifier Noise Pickup	158
9.1.3 Control Chip Layout	159
9.1.4 Operation Close to Maximum Duty Cycle	160
9.1.5 Light Load Operation	161
9.1.6 Current Waveform Filtering	161
9.1.7 Current Loop Instability	162
9.1.8 Voltage Loop Instability	162
9.1.9 Instability Summary	163
9.2 Noise Measurement Techniques	164
9.2.1 Switching Power Supply Noise Generation	164
9.2.2 Measurement of Output Noise	165
9.2.3 Filtering the Output Noise	167
9.2.4 Noise Measurement Summary	168

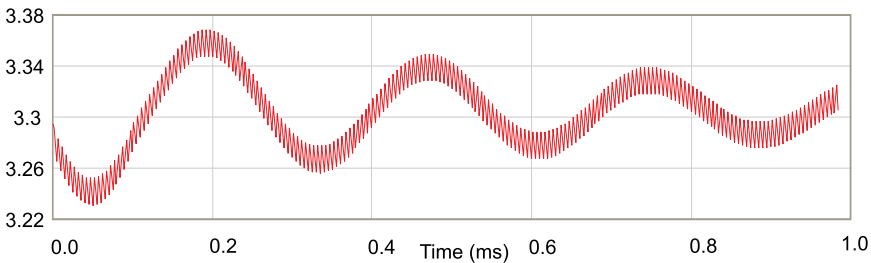
6.7.3 Buck Converter Transient Response – too Little Gain

Fig. 6.30(a) shows a step-load transient response for a buck converter with voltage-mode control. The converter has a 4 kHz oscillatory response, indicating insufficient phase margin.

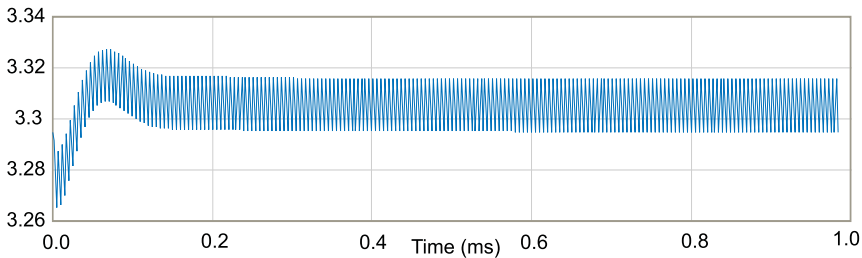
In the previous section, reduction of loop gain was the proper solution to improve the response. However, in the buck converter case shown here, *reducing the gain* makes the stability problem *worse*. The step load response is even more undamped, as shown in Fig. 6.30b.



(a) Original Transient



(b) Reduced Gain



(c) Increased Gain

Figure 6.30: Transient load response of buck converter (a) before (b) after gain reduction and (c) after gain increase.

The proper solution in this case is to *increase* the gain of the feedback loop, resulting in the response of Fig. 6.30(c).

Looking at the loop gains of Fig. 6.31 gives us insight into what is happening. The green curve shows the original gain, the red curve shows the decreased gain, and the blue curve shows increased gain. The phase margins at the crossover frequencies give us the characteristic transient response. For the red curve, we can see that the loop crossover has dropped close to the LC filter resonant frequency, resulting in only 10 degrees phase margin.

For the blue curve, the crossover is moved to a much higher frequency, away from the LC filter resonance, where the phase delay is much less. This results in a stable system with 60 degrees phase margin. Further changes can be made to stop the phase dropping down close to -180 degrees, and the loop gain information provides clear information on how to proceed.

For the buck converter with voltage-mode control, just above the filter resonance, the phase improves as the frequency increases. This information is available from the loop gain, but not from the step-load response.

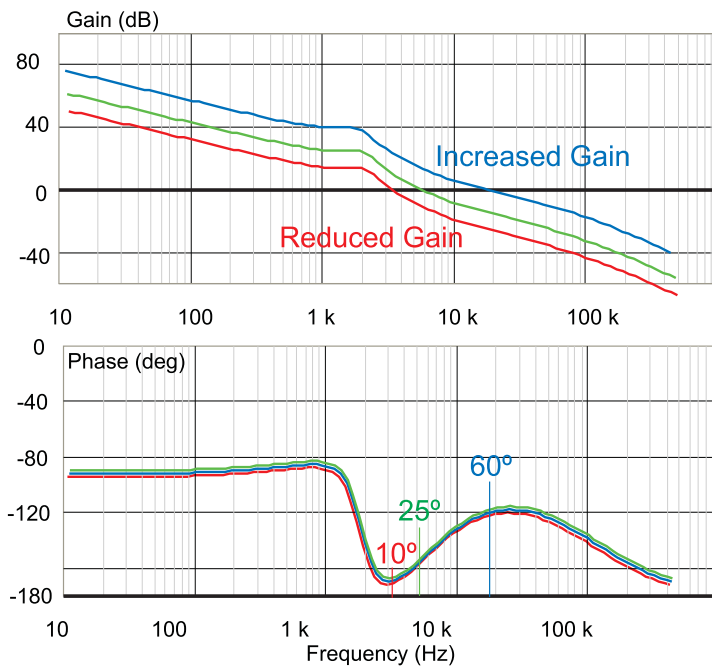


Figure 6.31: Loop gain and phase of the buck converter.

7.1 Designing with the TL431

Power supply designers are constantly striving to reduce cost. Prices have been driven down to very low levels, and compromises must often be made to meet both performance and cost goals. One of the targets for cost reduction is always the control loop. Designers are typically so focused on power processing that control design is often an afterthought, and not viewed as crucial to performance. Only later in the design process does the importance of a good controller become apparent.

One of the ways to reduce the cost of the feedback loop is to utilize the TL431 controller. While not offering as good a gain-bandwidth product as a standalone amplifier, this three-terminal part includes a reference. It also takes up little board space, and has become widespread in the industry as a way to achieve reasonable performance at a reduced cost.

In this section, we'll look at the complications involved in using the TL431, especially when it is configured with an optocoupler to provide isolation in the feedback loop.

7.1.1 Operational Amplifier Feedback

For the best performance, the preferred approach to feedback control compensation is an error amplifier and a precision reference. For nonisolated converters, the amplifier and reference may be included in the PWM control chip. They are usually of sufficient quality to meet demanding performance standards.

Current-mode control is the best way to control converters, and is used by most power supply designers. For this type of control, the optimal compensation network is a type II amplifier, an example of which is shown in Figure 7.1. In this configuration, a conventional operational amplifier is used to amplify the difference between the output voltage of the power supply and a fixed reference voltage.

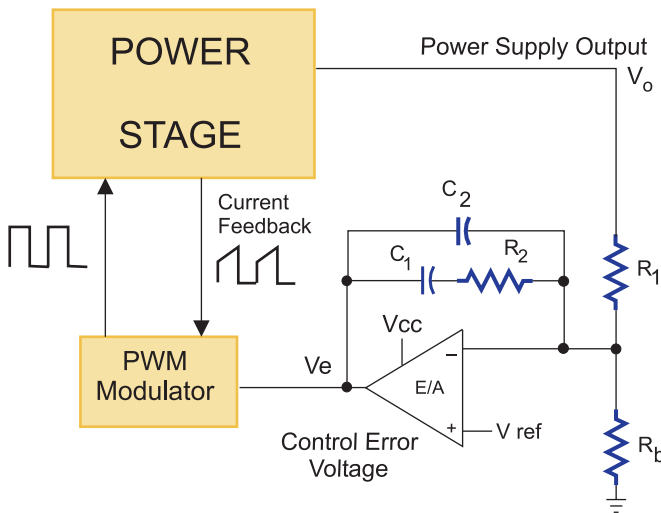


Figure 7.1: Type II compensation feedback

The amplifier is supplied by a separate V_{cc} , and the operation is not affected by variations in the supply voltage due to a good power supply rejection ratio.

Figure 7.2 shows the typical compensation curve for a type II amplifier. At low frequencies, the circuit acts like an integrator, utilizing components C_1 and R_1 to provide high gain. Resistor R_b provides the correct dc regulation level, but due to the virtual ground at the input of the error amplifier, it does not appear in any of the gain equations.

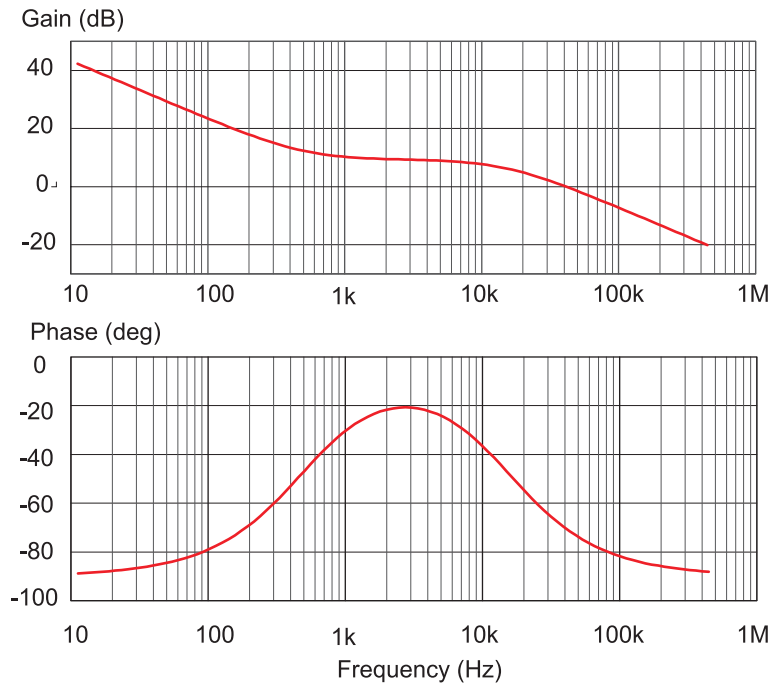


Figure 7.2: Type II compensation bode plot

At a frequency typically several times less than the loop gain crossover, a zero is introduced in the transfer function and the midband gain of the compensator is a simple expression given by the ratio of R_2 and R_1 . At a higher frequency, selected according to the power stage characteristics, the circuit again forms an integrator, the gain determined by R_1 and C_2 . Further details about the choice of these compensation parameters are given in chapter 5 of this book.

7.1.1 Using the TL431 as a Type II Amplifier

Although the TL431 is advertised as a transconductance amplifier, and is unusual in its configuration in a 3-pin package, it can be used as a standard Type II error amplifier if connected properly.

Figure 7.3 shows the required circuit connection for the TL431 to be used as a standard error amplifier. There are three differences found when using this part versus a standard operational amplifier:

1. A pullup resistor must be used on the output. The value of this resistor must be chosen to provide sufficient bias current to the device under all circuit conditions. Furthermore, the output of the amplifier must be kept above a minimum value required to provide the bias.
2. The reference is included in the part. It is a very good reference for the price.
3. The open loop gain, and drive capability are less than that of a good op amp. However, if you keep the impedances around the amplifier high, it will work well.

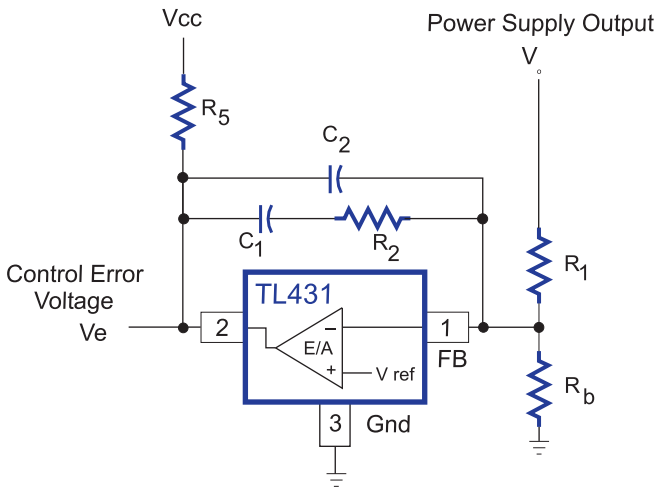


Figure 7.3: TL431 used as a type II amplifier

If the TL431 is configured as shown in Figure 7.3, and the rules above are obeyed, the design procedure is exactly the same as for a standard type II amplifier. Notice that the pullup resistor for the output of the TL431 is biased with a **regulated voltage**, and **not connected directly** to the output voltage.

7.1.2 Solution with Isolation

While the circuit shown in Fig. 7.3 is easy to design with and very predictable, it is not usually used in industry in this manner. The circuit that has become very widespread is where the TL431 is used in conjunction with an optocoupler to provide feedback loop isolation, as shown in Figure 7.4.

In this circuit, the output of the TL431 is powered through the resistor R_5 and the optocoupler diode, connected in series with the power supply output. This apparently subtle change has a big effect on the way the circuit works.